

(19) World Intellectual Property
Organization
International Bureau



(43) International Publication Date
23 October 2003 (23.10.2003)

PCT

(10) International Publication Number
WO 2003/088364 A3

(51) International Patent Classification⁷: **H01L 29/78**,
29/10, 29/36

(21) International Application Number:
PCT/TB2003/001465

(22) International Filing Date: 10 April 2003 (10.04.2003)

(25) Filing Language: English

(26) Publication Language: English

(30) Priority Data:
0208833.4 18 April 2002 (18.04.2002) GB

(71) Applicant (for all designated States except US): **KONIN-
KLJKE PHILIPS ELECTRONICS N.V.** [NL/NL];
Groenewoudseweg 1, NL-5621 BA Eindhoven (NL).

(72) Inventor; and

(75) Inventor/Applicant (for US only): **PEAKE, Steven, T.**
[GB/GB]; Philips Intellectual Property & Standards, Cross
Oak Lane, Redhill, Surrey RH1 5HA (GB).

(74) Agent: **SHARROCK, Daniel, J.**; Philips Intellectual
Property & Standards, Cross Oak Lane, Redhill, Surrey
RH1 5HA (GB).

(81) Designated States (*national*): AE, AG, AL, AM, AT, AU,
AZ, BA, BB, BG, BR, BY, BZ, CA, CH, CN, CO, CR, CU,
CZ, DE, DK, DM, DZ, EC, EE, ES, FI, GB, GD, GE, GH,
GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC,
LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW,
MX, MZ, NI, NO, NZ, OM, PH, PL, PT, RO, RU, SC, SD,
SE, SG, SK, SL, TJ, TM, TN, TR, TT, TZ, UA, UG, US,
UZ, VC, VN, YU, ZA, ZM, ZW.

(84) Designated States (*regional*): ARIPO patent (GH, GM,
KE, LS, MW, MZ, SD, SL, SZ, TZ, UG, ZM, ZW),
Eurasian patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM),
European patent (AT, BE, BG, CH, CY, CZ, DE, DK, EE,
ES, FI, FR, GB, GR, HU, IE, IT, LU, MC, NL, PT, RO,
SE, SI, SK, TR), OAPI patent (BF, BJ, CF, CG, CI, CM,
GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG).

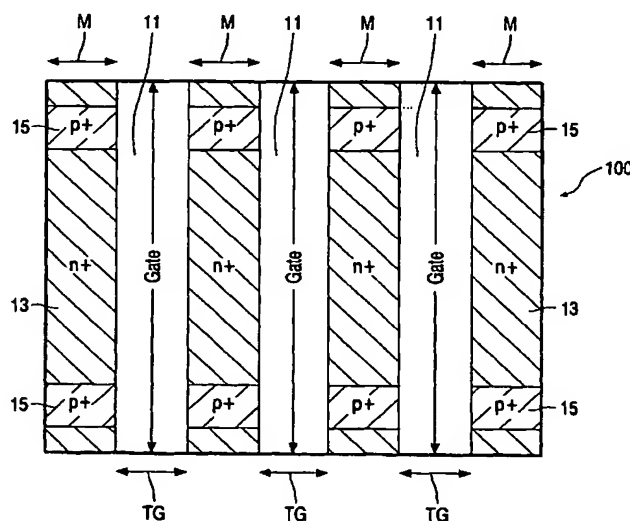
Published:

— with international search report

(88) Date of publication of the international search report:
26 February 2004

[Continued on next page]

(54) Title: TRENCH-GATE SEMICONDUCTOR DEVICES



(57) Abstract: A trench-gate vertical power transistor in which the trench-gates (11) are parallel stripes which extend across the active area (100). Source regions (13) and ruggedness regions (15) extend to a source contact surface as alternating stripe areas having a width perpendicular to and fully between each two adjacent parallel stripe trench-gates (11). The ruggedness regions (15) are more heavily doped than the source regions and this enables an increased length of the source regions with a consequent reduction in specific resistance of the transistor. For example, the mesa width (13,15) and the trench-gate (11) width may both be about 0.4 μ m, the ruggedness region (15) length may be about 1 μ m and the source region (13) length may be about 20 μ m. The doping concentration of the p type ruggedness regions (15) may be approximately 10 times greater than the doping concentration of the n type regions (13), for example about 10₂₁ cm⁻³ and about 10₂₀ cm⁻³ respectively.

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INTERNATIONAL SEARCH REPORT

PCT/IB 03/01465

13 OCT 2003

A. CLASSIFICATION OF SUBJECT MATTER

IPC 7 H01L29/78 H01L29/10 H01L29/36

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

IPC 7 H01L

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

EPO-Internal, WPI Data, INSPEC

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	US 5 072 266 A (BULUCEA C ET AL) 10 December 1991 (1991-12-10) cited in the application column 3, line 63 -column 4, line 27; figures 2A, 3A-3B ---	1
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A	EP 0 199 293 A (GEN ELECTRIC) 29 October 1986 (1986-10-29) abstract; figures 1-5 -----	1

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Date of the actual completion of the international search

21 November 2003

Date of mailing of the international search report

02/12/2003

Name and mailing address of the ISA

European Patent Office, P.B. 5818 Patentlaan 2
NL - 2280 HV Rijswijk
Tel. (+31-70) 340-2040, Tx. 31 651 epo nl,
Fax: (+31-70) 340-3016

Authorized officer

Morvan, D

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PCT/IB 03/01465

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